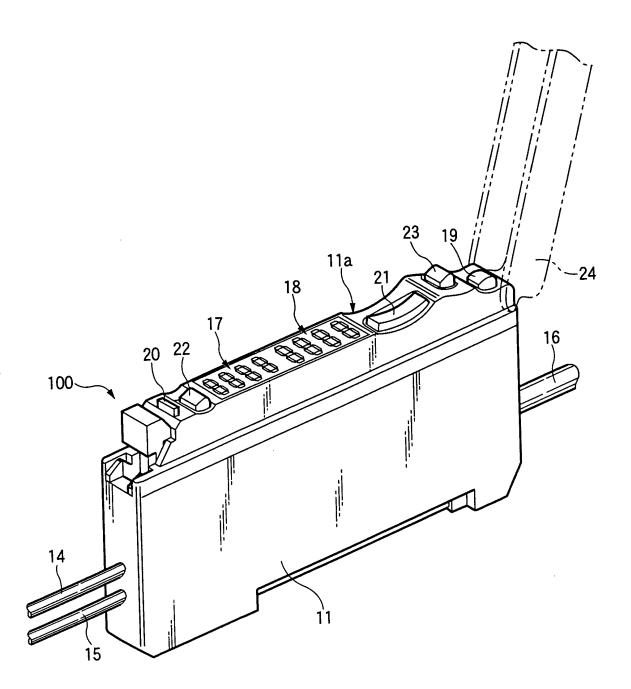
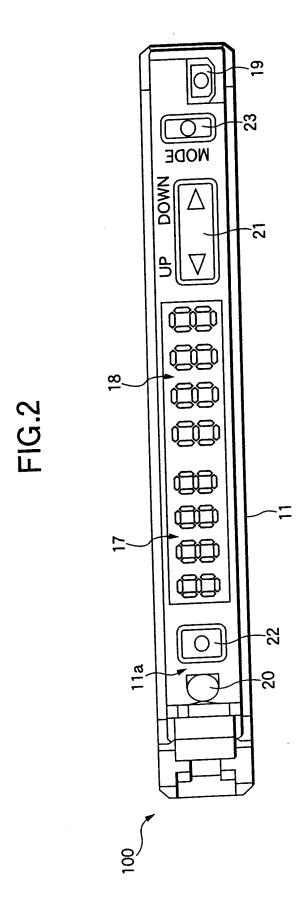
FIG.1





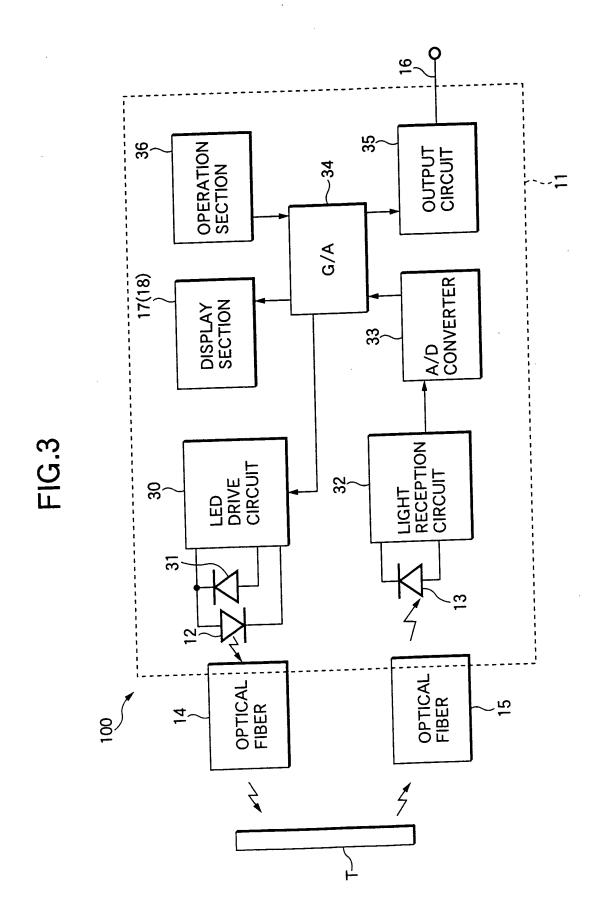


FIG.4

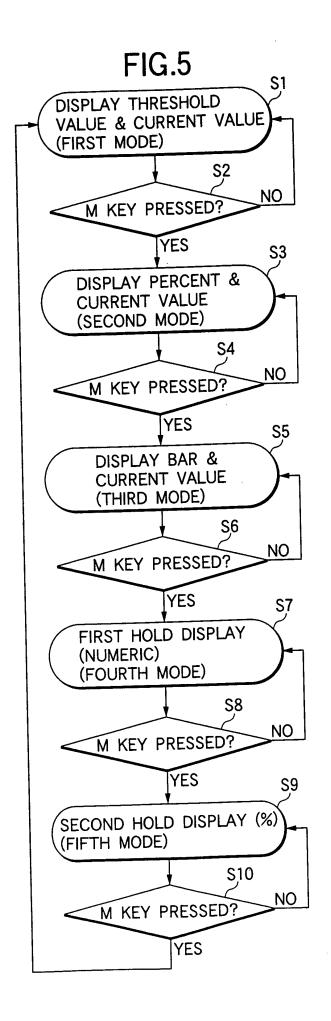
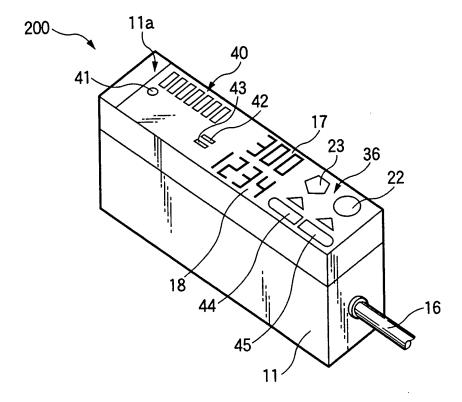
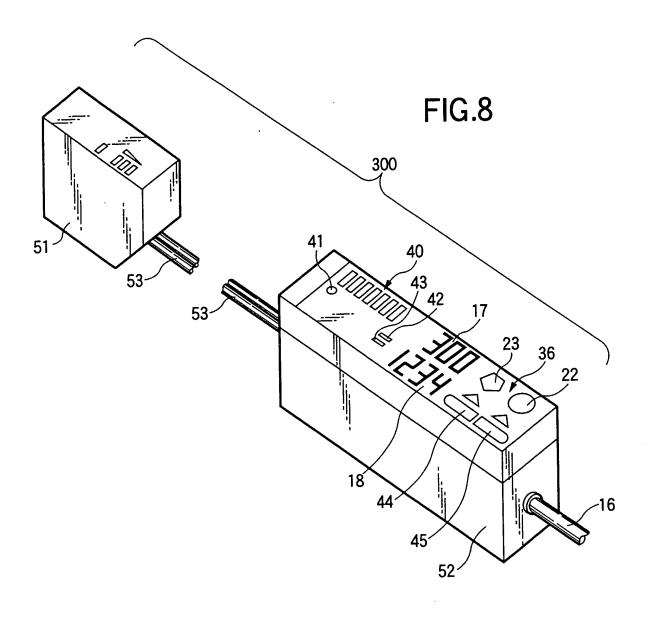
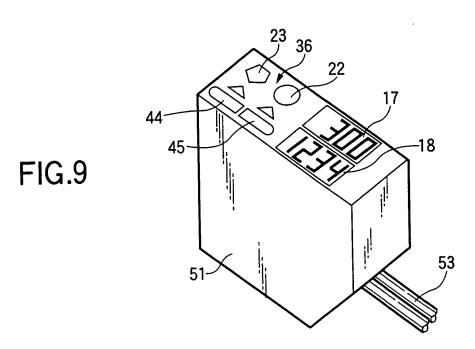


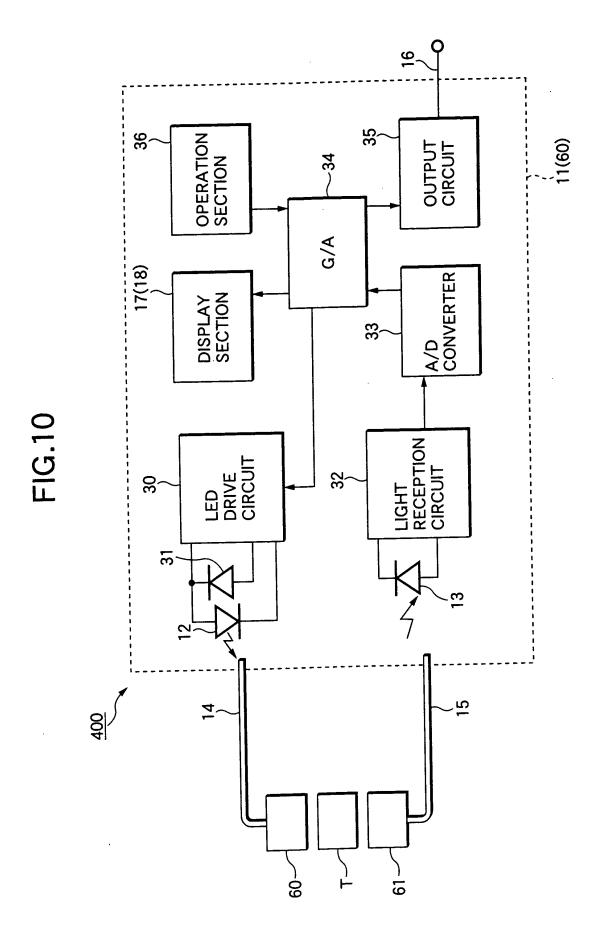
FIG.6



OPERATION SECTION 36 OUTPUT CIRCUIT 35 DISPLAY SECTION CPU 17(18) S PLS A/D CONVERTER 33 VARIABLE POWER SUPPLY CIRCUIT DP ‡ 47 # FIG.7 LED LIGHT EMISSION CIRCUIT RS POWER SUPPLY CIRCUIT 46 \$ LIGHT RECEPTION CIRCUIT DRIVE CIRCUIT VREF 32 30 LIGHT ⁷LIGHT 200







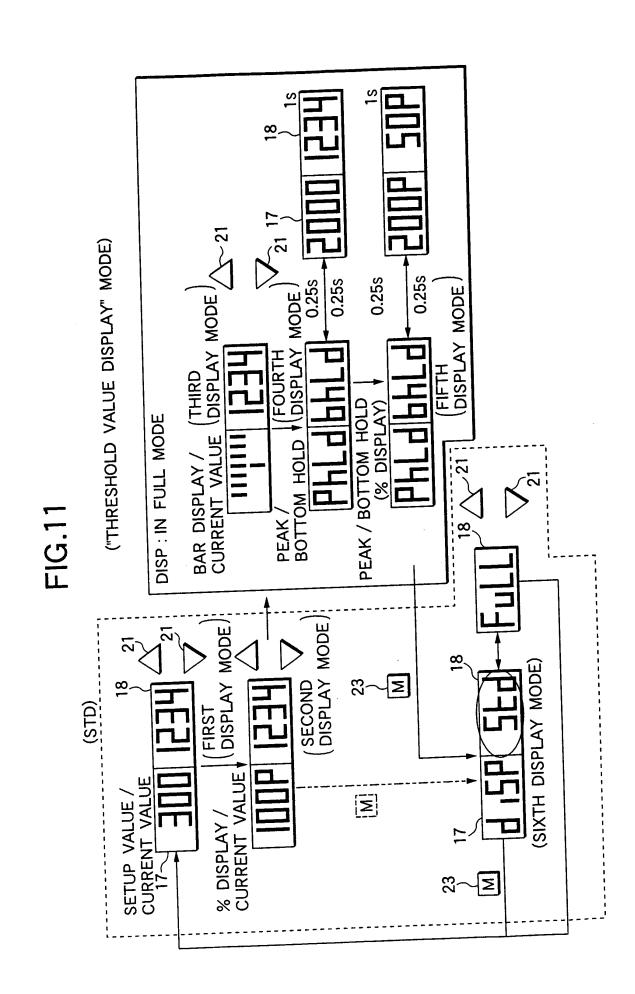
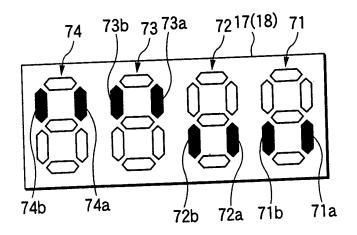


FIG.12



PRIOR ART FIG.13

